



PATENTS
112055-0073U
17732-67070.00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re The Application of:
Pravas Pradhan et al.

Serial No.: 10/720,857

Filed: November 24, 2003

For: CURRENT TRANSFER LOGIC

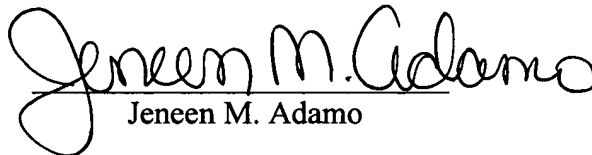
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) Examiner: Not yet assigned
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) Art Unit: 2816
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Cesari and McKenna, LLP
88 Black Falcon Avenue
Boston, MA 02210
March 9, 2004

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I hereby certify that the following Information-Disclosure Statement is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 9, 2004.


Jeneen M. Adamo

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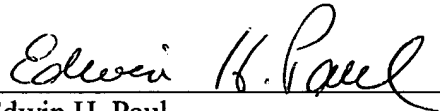
In keeping with the duty of candor and good faith owed to the Patent and Trademark Office, Applicants wish to bring to the Examiner's attention the references listed on the accompanying form PTO-1449. A copy of each listed reference is enclosed.

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To the extent required by 37 C.F.R. §1.98(a)(3), Applicants have described what they consider to be the relevance of any foreign-language reference. The Office may find additionally relevant material in these or other references.

Please charge any additional fee occasioned by this paper to our Deposit Account No. 03-1237.

Respectfully submitted,

A handwritten signature in cursive script, reading "Edwin H. Paul", is written over a horizontal line.

Edwin H. Paul
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet

1

of

2

Application Number

10/720.857

Filing Date

November 24, 2003

First Named Inventor

Pravas Pradhan et al.

Group Art Unit

2816

Examiner Name _____

Not yet assigned

Attorney Docket Number

112055-0073U

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**Examiner
Signature**

Date Considered

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known

Application Number	10/720,857
Filing Date	November 24, 2003
First Named Inventor	Pravas Pradhan et al.
Group Art Unit	2816
Examiner Name	Not yet assigned
Attorney Docket Number	112055-0073U

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	7.	SEEVINCK, Evert, Current-Mode Techniques for High-Speed VLSI Circuits with to Current Sense Amplifier for CMOS SRAM's, IEEE Journal of Solid-State Circuits, Vol. 26, No. 4, April 1991	
	8.	SIM, Jae-Yoon, A 1-Gb/s Bidirectional I/O Buffer Using the Current-Mode Scheme, IEEE Journal of Solid-State Circuits, Vol. 34, No. 4, April 1999	

Examiner
Signature

Date
Considered

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